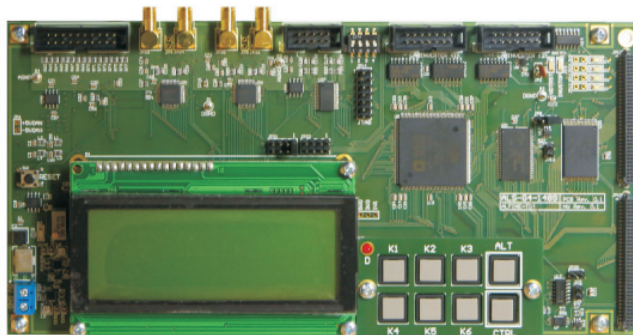


TECHNICAL INFORMATION

ALS-G4-1489

DSP EVALUATION BOARD FOR INDUSTRIAL MEASUREMENT AND CONTROL PURPOSES



Board Features

■ DSP

Analog Devices floating point DSP
ADSP-21489 SHARC® (5 Mb SRAM, 400 MHz Core Clock, 800 MIPS, 2.4 GFLOPS)

■ External Data Bus

16 bit

■ External Address Bus

24 bit

■ External Memory

SRAM: 4 Mb (256 k x 16)
Boot Memory - Serial Flash: 32 Mb

■ Analog-Front-End

A/C:

1) 8 channels, simultaneous sampling, 18 bit, 200 kSps, +/- 5.0 V, built-in 23 kHz anti-alias filters

2) 2x2 channels, simultaneous sampling, 18 bit, 500 kSps, fully differential inputs, true bipolar +/- 2.048 V, programmable gain (4 steps: 1, 2, 5, 10 V/V - in a single ended input mode)

C/A: 4 channels, 12 bit, $t_{set} = 7 \mu s$, true bipolar output: +/- 2.5 V

■ Power Electronics Interface¹⁾ & Digital General Purpose Interface

Up to 12 PWM channels - unpaired mode / up to 6 PWM Channels - paired mode

Up to 12 Output FLAGS

External WATCHDOG TIMER

■ User Console

LCD - alphanumeric: 4x20 characters

Keyboard: 10 keys

■ Communication Interfaces

JTAG

DSP built-in interfaces: UART/SPI/SPORT

■ Expansion Headers²⁾

High density, 2 (one populated)

■ Power requirements

External power supply: regulated, 5.0 V (+/- 5 %) / 2.5 A

¹⁾ Compatible with P3-5.0/550MFE LABINVERTER

²⁾ Analog and digital **Functionality Extension Micro Cards (FEMC)** available as option: Analog-Front-End Cards, Memory Cards, Resolver-to-Digital-Converter Card, UART Card, USB Card, User Defined Card

Available Software - supported by ALFINE-TIM:
VisualDSP++® 5.1 compatible exemplary software and compiled C Library Modules

External Development Tools - supported by Analog Devices Inc. : VisualDSP++®, CrossCore® Embedded Studio, JTAG Emulators

Card specification can be changed without notices !



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