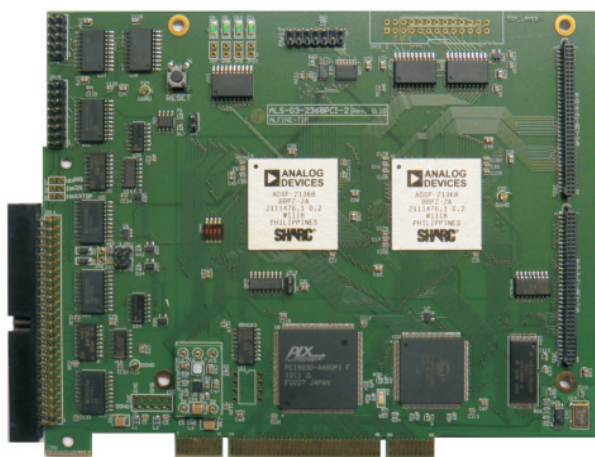




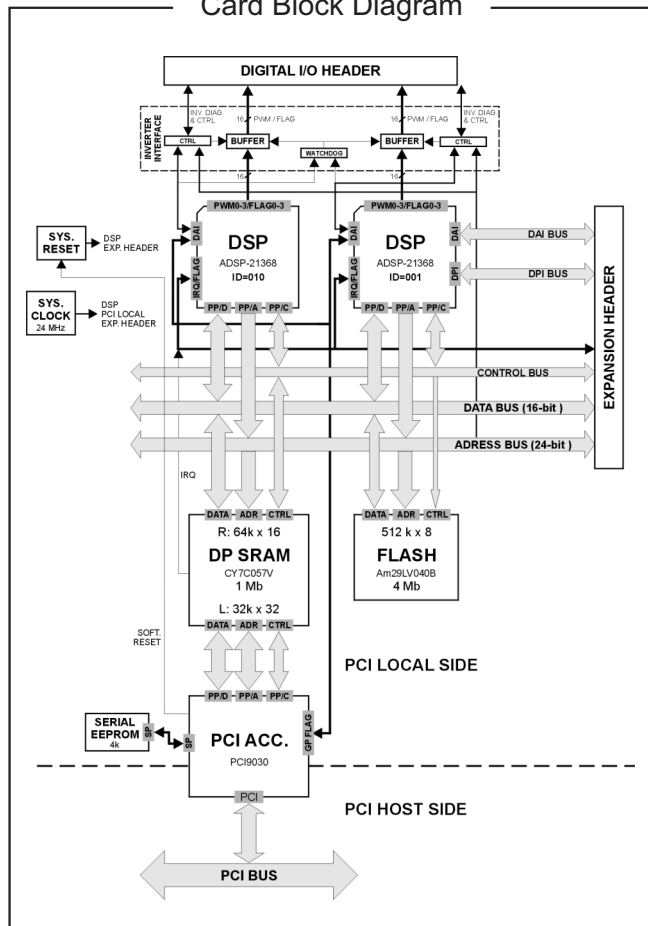
## TECHNICAL INFORMATION

# ALS-G3-2368PCI-2

## DUAL PROCESSOR DSP/PCI CARD FOR INDUSTRIAL MEASUREMENT AND CONTROL PURPOSES



Card Block Diagram



### Card Features

- **Card compliant with PCI Rev. 2.0**
- **DSP Block**  
Two Analog Devices SHARC Family floating point DSPs ADSP-21368 (2 Mb SRAM, 333 MHz Core Clock, 666 MIPS, 2 GFLOPS)
- **Shared Memory**  
Dual Port SRAM: 1Mb (32kx32 - Host Side, 64x16 -Local Side);  
Flash Memory (Boot Memory): 512kx8
- **Power Electronics & Digital GP Interface**  
32-channel PWM Generator / 32 Output Digital Lines (DSP FLAGS)
- **PCI Accelerator**  
PLX Tech. PCI9030 configured in Non-Multiplexed Mode; PCI (Host) Side: 33 MHz / 32-bit, Local Side: 24 MHz / 32-bit
- **Communication Interfaces: PCI, JTAG**
- **User Console Header (as option)**
- **Expansion Header**
- **Power requirements**  
5V / 2.5 A
- **Standalone mode available (as option)**

### Available Software and Compliant Development Tools

- **HOST Side - supported by PLX Tech.:**  
WINDOWS XP/7 Drivers, Diagnostics Software and C/C++ Library Modules
- **LOCAL Side - supported by Analog Devices/CROSSCORE:** VisualDSP++, JTAG Emulators: ADDZ-USB-ICE, ADDZ-HPUSB-ICE
- **Card specific library functions - supported by ALFINE-TIM**

Card specification could be object of changes without notices !

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