



TECHNICAL INFORMATION

ALS-G3-1369

DSP EVALUATION BOARD FOR INDUSTRIAL MEASUREMENT AND CONTROL PURPOSES

Board Features

■ DSP

Analog Devices floating point DSP
ADSP-21369 (2 Mb SRAM, 400 MHz Core
Clock, 800 MIPS, 2.4 GFLOPS)

■ External Data Bus

Non-FLAGS/PWM Mode: 32 bit
FLAGS/PWM Mode: 16 bit

■ External Address Bus

Banked, 24 bit

■ External Memory

SRAM: 4Mb (256k x 16); optional - MRAM:
4Mb (256k x 16)
Parallel Flash-Boot Memory: 512k x 8b

■ Analog Front End

A/C: 12 channels, simultaneous sampling,
16 bit, 250 kSps, +/- 5.0 V or +/-10V
C/A: 4 channels, 14 bit, $t_{set} = 8 \mu s$, +/- 5.0 V

■ Power Electronics Interface & Digital GP Interface

Up 16 PWM channels - unpaired mode / Up
8 PWM Channels - paired mode
Up 16 I/O FLAG
WATCHDOG TIMER



■ User Console

LCD: 20x4 char.
Keyboard: 10 keys

■ Communication Interfaces

JTAG
DSP built-in interfaces: UART/SPI/SPORT

■ Expansion Header

High density, 2

■ Power requirements

External power supply: regulated, 5.0V(+/-5%)
/1.5 A

Available Software - supported by ALFINE-TIM

Example Software and compiled C Library Modules

External Development Tools - supported by Analog

Devices/CROSSCORE: VisualDSP++ Rev. 5.0, JTAG
Emulators: ADDZ-HPUSB-ICE, ADDZ-USB-ICE



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Card specification can be changed without notices !