Operational Amplifiers—Precision and General-Purpose, <50 MHz

Part Number			Shutdown	Supply Voltage (V)		Technology Feature[2]	Rail-to-Rail[3]		A_{CL} Min	BW @ A_{CL} Min (MHz)	Slew Rate (V/µs)	V_{OS} Max (mV)	TCV_{OS} Typ (µV/°C)	CMRR Min (dB)	PSRR Min (dB)	A_{VO} Min (dB)	Noise @ 1 kHz (nV/√Hz)	Noise @ 1 kHz (pA/√Hz)	I_S/Amp Max (mA)	I_B Max (pA)	I_{SC} (mA)	Temp Range[4]	MicroCSP[5]	SC70[5]	SOT-23[5]	MSOP[5]	SOIC[5]	LFCSP[6]	BBFPP	TSSOP[5]	PDIP[6]	Price @ 1k (OEM U.S.)
Single	Dual	Quad		Min	Max[1]		In	Out																								
Max Voltage < 6 V																																
AD8500				1.8	<6	DigiTrim®	•	•	1	0.007	0.004	1	3	75	90	98	190	0.1	0.001	10	5	H		S								0.71
	AD8502	AD8504		1.8	<6		•	•	1	0.007	0.004	3	5	67	85	98	190	0.1	0.001	10	5	H			D						Q		0.70/1.00
AD8505	AD8506	AD8508		1.8	<6	ZCO	•	•	1	0.095	0.013	2.5	2	90	100	105	45	0.015	0.020	10	45	H	S/D/Q		S	D					Q		0.47/0.57/1.02
AD8515				1.8	<6		•	•	1	5	2.7	6	4	60	65	113	22	0.05	0.550	30	20	H		S	S							0.28
AD8531	AD8532	AD8534		2.7	<6		•	•	1	3	5	25	20	38	45	83	45	0.05	0.700	50	250[1]	I		S	S	D	D/Q			D/Q		0.27/0.31/0.60
AD8538	AD8539			2.7	<6	ZD	•	•	1	0.43	0.35	0.013	0.03	115	105	115	50		0.180	25	25	H			S	D	S/D					0.50/0.72
AD8541	AD8542	AD8544		2.7	<6		•	•	1	1	0.92	6	4	40	65	86	42	0.1	0.065	60	60	H		S	S	D	S/D/Q			D/Q		0.27/0.38/0.54
AD8551	AD8552	AD8554		2.7	<6	ZD	•	•	1	1.5	0.4	0.005	0.005	120	120	125	42	0.002	0.975	50	50	H				S	S/D/Q			D/Q		1.22/1.94/3.43
AD8571	AD8572	AD8574		2.7	<6	ZD	•	•	1	1.5	0.4	0.005	0.005	120	120	125	51	0.002	0.975	50	50	H				S	S/D/Q			D/Q		1.11/1.78/3.40
AD8591	AD8592		•	2.7	<6		•	•	1	3	5	25	20	38	45	83	45	0.05	0.700	50	250[1]	I			S		D					0.29/0.39
	AD8602	AD8604		2.7	<6		•	•	1	8.2	5.2	6	2	56	56	86	33	0.05	1.000	200	30	H				D	D/Q			Q		0.44/0.90
AD8601A	AD8602A	AD8604A		2.7	<6	DigiTrim	•	•	1	8.2	5.2	0.5	2	74	67	89	33	0.05	1.000	60	30	H			S	D	D/Q			Q		0.63/0.83/1.13
AD8605	AD8606	AD8608		2.7	<6	DigiTrim	•	•	1	10	5	0.3	1	85	80	109	8	0.01	1.200	1	80	H	S/D		S	D	D/Q			Q		0.68/1.19/1.58
AD8603	AD8607	AD8609		1.8	<6	DigiTrim	•	•	1	0.4	0.1	0.3	1	85	80	112	25	0.05	0.050	1	80	H			S	D	D/Q			Q		0.68/1.02/1.85
AD8615	AD8616	AD8618		2.7	<6	DigiTrim	•	•	1	24	12	0.5	1.5	80	70	105	10	0.05	2.000	1	150	H			S	D	D/Q			Q		0.76/1.29/2.29
AD8613	AD8617	AD8619		1.8	<6		•	•	1	0.4	0.1	2.2	1	68	67	107	25	0.05	0.040	1	80	H		S	S	D	D/Q			Q		0.46/0.71/1.11
AD8628	AD8629	AD8630		2.7	<6	ZD	•	•	1	2.5	1	0.005	0.002	120	115	125	22	0.005	1.100	100	50	H			S	D	S/D/Q			Q		0.96/1.47/2.73
	AD8646	AD8648		2.7	<6		•	•	1	24	11	2.5	1.8	67	63	104	8		2.000	1	120	H				D	D/Q			Q		0.61/0.88
	AD8647		•	2.7	<6		•	•	1	24	11	2.5	1.8	67	63	104	8		1.500	1	120	H				D						0.71
AD8651	AD8652			2.7	<6	DigiTrim	•	•	1	50	41	0.35	4	80	76	100	4.5[1]	0.025	14.000	10	80	H				S/D	S/D					1.13/1.99
AD8655	AD8656			2.7	<6	DigiTrim	•	•	1	28	11	0.25	0.4	85	88	100	2.7[1]		4.500	10	220	H				S/D	S/D					0.71/1.11
AD8691	AD8692	AD8694		2.7	<6		N	•	1	10	5	2	1.3	70	80	108	8	0.05	1.050	1	80	H		S	S	D	D/Q			Q		0.44/0.60/0.80
ADA4051-1	ADA4051-2			1.8	<6	ZD	•	•	1	0.125	0.06	0.015	0.02	110	110	115	95	0.1	0.017	70	15	H		S	S	D		D					0.93/1.47
	ADA4500-2			2.7	<6	ZCO	•	•	1	10	5.5	0.12	0.9	95	98	105	14.5	0.58	1.750	2	75	H				D		D					0.98
ADA4505-1	ADA4505-2	ADA4505-4		1.8	<6	ZCO	•	•	1	0.050	0.006	3	2	90	100	105	65	0.02	0.010	2	40	H	S/D/Q		S	D					Q		0.41/0.57/1.01
ADA4528-1	ADA4528-2			2.2	<6	ZD	•	•	1	4	0.45	0.0025	0.002	137	130	127	5.9	0.5	1.800	200	25	H				S/D		S/D					0.98/1.52
	ADA4691-2	ADA4691-4	•	2.7	<6		N	•	1	3.6	1.3	2.5	1	75	80	95	16	0.05	0.225	5	55	H	D					D/Q				0.57/0.90
	ADA4692-2	ADA4692-4		2.7	<6		N	•	1	3.6	1.3	2.5	1	75	80	95	16	0.05	0.225	5	55	H					D	D		Q		0.55/0.85

[1] Check data sheet for condition.
[2] BP = bipolar, ZD = zero drift, ZCO = zero crossover, *i*CMOS = 16 V or 33 V CMOS, OVP = overvoltage protection, LT = laser trim, ZZ = Zener zap, P = planned.
[3] N = negative rail input, P = positive rail input.
[4] C = commercial (0°C to +70°C), H = extended industrial (–40°C to +125°C), I = industrial (–40°C to +85°C).
[5] S = single, D = dual, Q = quad.
[6] In development.

For more information on ADI amplifiers, visit *www.analog.com/amps*.

ANALOG DEVICES

Part Number			Shutdown	Supply Voltage (V)		Technology Feature[2]	Rail-to-Rail[3]		A_{CL} Min	BW @ A_{CL} Min (MHz)	Slew Rate (V/µs)	V_{OS} Max (mV)	TCV_{OS} Typ (µV/°C)	CMRR Min (dB)	PSRR Min (dB)	A_{VO} Min (dB)	Noise @ 1 kHz (nV/√Hz)	Noise @ 1 kHz (pA/√Hz)	I_S/Amp Max (mA)	I_B Max (pA)	I_{SC} (mA)	Temp Range[4]	MicroCSP[5]	SC70[6]	SOT-23[6]	MSOP[5]	SOIC[5]	LFCSP[5]	BBFPP	TSSOP[5]	PDIP[5]	Price @ 1k (OEM U.S.)
Single	Dual	Quad		Min	Max[1]		In	Out																								
Max Voltage < 18 V																																
AD8519	AD8529			2.7	<12	BP	N	•	1	8	2.9	1.1	2	70[1]	60	94	10	0.4	1.200	300 nA	70	H		S	S	D	S/D					0.94/1.24
	AD8546	AD8548		3	<18		•	•	1	0.2	0.07	3	3	80	90	88	50	0.1	0.022	20	10	H				D	D					0.78/1.05
AD8565	AD8566	AD8567		4.5	<16	BP	•	•	1	5	6	10	5	54	70	69	26	0.8[1]	0.850	600 nA	35[1]	I		S		D		Q		Q		0.56/0.71/0.96
AD8638	AD8639			5	<16	ZD	N	•	1	1.5	2	0.009	0.03	127	127	130	60		1.500	75	37	H			S	D	S/D	D				1.08/1.89
	AD8657	AD8659		3	<18	DigiTrim	•	•	1	0.2	0.07	0.35	4	95	105	110	50	0.1	0.022	20	10	H				D	D					0.95/1.35
AD8661	AD8662	AD8664		5	<16	DigiTrim	N	•	1	4	3.5	0.16	4	90	95	106	12	0.1	1.550	1	140	H				D	S/D/Q	S		Q		1.08/1.37/2.23
AD8665	AD8666	AD8668		5	<16		N	•	1	4	3.5	2.5	3	90	98	130	10	0.1	1.550	1	140	H			S	D	S/D/Q			Q		0.83/0.93/1.75
AD8663	AD8667	AD8669		5	<16	DigiTrim	N	•	1	0.54	0.6	0.3	1.5	87	95	115	23	0.05	0.285	0.3 typ	50	H				D	S/D/Q	S		Q		1.17/1.58/2.70
	ADA4665-2			5	<16		•	•	1	1.2	1	6	3	55	70	85	32		0.400	1	10	H				S	S					0.70
OP162	OP262	OP462		2.7	<12	LT	N	•	1	15	13	0.325	1	70	60	97	9.5	0.4	0.800	500 nA	30[1]	H				S	S/D/Q			S/D/Q		1.69/2.23/4.11
	OP281	OP481		2.7	<12	BP	N	•	1	0.105	0.028	1.5	10	65	76	74	85	1	0.005	10 nA	12	I					S/D			S/D		2.79/3.65
OP191	OP291	OP491		2.7	<12	OVP	•	•	1	1.5	0.5	0.5	1.1	75	80	88	42	0.8	0.420	65 nA	16	H					S/D/Q			Q	Q	1.72/2.22/3.89
OP196	OP296	OP496		3	<15	LT	•	•	1	0.45	0.3	0.3	1.5	65[1]	110	109	26	0.19	0.060	50 nA	4[1]	H					S/D/Q			D/Q	D/Q	1.51/1.89/2.71
Max Voltage < 44 V																																
AD820A	AD822A	AD824A		5	<36	JFET, LT	N	•	1	1.9	3	2	2	70	70	114	16	0.008	0.900	25	45	I				S/D	S/D/Q				S/D	1.82/2.84/4.87
AD820B	AD822B			5	<36	JFET, LT	N	•	1	1.9	3	1	2	74	70	114	16	0.008	0.900	10	45	I				D	S/D				S/D	2.66/4.23
	ADA4661-2			3	<20.5	Digitrim	•	•	1	4	2	0.15	.6	115	120	120	18	0.36	0.725	15	220	H				D		D				1.13
	ADA4666-2			3	<20.5	Digitrim	•	•	1	4	1.7	2.2	.6	64	120	105	18	0.36	0.725	15	220	H				D		D				0.85
AD817	AD826			±2.5	<±18				1	50	350	2	10	86	75	72	15[1]	1.5[1]	7.500	6.6 µA	90	I					S/D				S/D	1.76/2.43
AD8510A	AD8512A	AD8513A		±5	<±18	JFET, LT			1	8	20	1	1.7	86	86	101	8		2.500	80	70	H				S/D	S/D/Q			Q		0.95/1.49/3.71
AD8510B	AD8512B			±5	<±18	JFET, LT			1	8	20	0.4	1	86	86	101	8		2.500	80	70	H				S/D	S/D					2.33/4.76
AD8597	AD8599			±4.5	<±18	LT			1	10	16.8	0.12	0.8	120	120	110	1.07	1.9	5.700	200 nA	52	H					S/D	S				2.25/3.24
AD8610A	AD8620A			±5	<±13	JFET, LT			1	25	60	0.25	0.8	90	100	100	6	0.005	3.500	10	65	H				S	S/D					3.75/7.50
AD8610B	AD8620B			±5	<±13.5	JFET, LT			1	25	60	0.1	0.5	90	100	100	6	0.005	3.500	10	65	H				S	S/D					9.86/16.70
	AD8622	AD8624		±2.5	<±18	LT		•	1	0.56	0.48	0.125	0.5	125	125	125	11	0.15	0.250	200	40	H				D	D	Q		Q		1.96/3.56
AD8627	AD8626	AD8625		10	<27	JFET, LT	N	•	1	5	5	0.75	2.5	76	80	103	16	0.5	0.850	1	15[1]	I			S	D	S/D/Q			Q		1.60/2.63/4.09
AD8641	AD8642	AD8643		5	<27	JFET, LT	N	•	1	3.5	3	0.75	2.5	90	90	106	27.5	0.0005	0.290	1	12[1]	H			S	D	S/D/Q	Q				1.47/2.35/3.85
AD8671	AD8672	AD8674		±5	<±18	LT			1	10	4	0.075	0.3	100	110	120	2.8	0.3	3.500	12 nA	30	H				S/D	S/D/Q			Q		1.06/1.62/3.24
	AD8676A			±5	<±18	LT		•	1	10	2.5	0.1	0.2	111	106	123	2.8	0.3[1]	3.400	2 nA	35	H				D	D					1.66
	AD8676B			±5	<±18	LT		•	1	10	2.5	0.05	0.2	111	106	123	2.8	0.3[1]	3.400	2 nA	35	H				D	D					2.14
	AD8682	AD8684		±4.5	<±18	JFET, LT	P		1	3.5	9	1	10	70	92	86	36	0.01	0.250	20	10	I				D	D/Q			Q		1.66/2.44
ADA4000-1	ADA4000-2	ADA4000-4		±4	<±18	JFET, LT	P		1	5	20	1.7	2	80	82	100	16	0.01	1.650	40	28	H			S	D	S/D/Q			Q		0.73/1.31/2.22
	ADA4001-2			±4.5	<±18	JFET		•	1	10.2	25	1.5	5	96	96	104	8.8	0.003	3.000	30	50	H					D					1.09

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[2] BP = bipolar, ZD = zero drift, ZCO = zero crossover, *i*CMOS = 16 V or 33 V CMOS, OVP = overvoltage protection, LT = laser trim, ZZ = Zener zap, P = planned.
[3] N = negative rail input, P = positive rail input.
[4] C = commercial (0°C to +70°C), H = extended industrial (–40°C to +125°C), I = industrial (–40°C to +85°C).
[5] S = single, D = dual, Q = quad.
[6] In development.

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Part Number			Shutdown	Supply Voltage (V)		Technology Feature[2]	Rail-to-Rail[3]		A_{CL} Min	BW @ A_{CL} Min (MHz)	Slew Rate (V/µs)	V_{OS} Max (mV)	TCV_{OS} Typ (µV/°C)	CMRR Min (dB)	PSRR Min (dB)	A_{VO} Min (dB)	Noise @ 1 kHz (nV/√Hz)	Noise @ 1 kHz (pA/√Hz)	I_S/Amp Max (mA)	I_B Max (pA)	I_{SD} (mA)	Temp Range[4]	MicroCSP[5]	SC70[6]	SOT-23[6]	MSOP[5]	SOIC[5]	LFCSP[5]	BBFPP	TSSOP[5]	PDIP[5]	Price @ 1k (OEM U.S.)
Single	Dual	Quad		Min	Max[1]		In	Out																							
ADA4004-1	ADA4004-2	ADA4004-4		±5	<±18	LT			1	12	2.7	0.125	0.7	110	110	114	1.8	1.2[1]	2.200	90 nA	25	H			S	D	S/D/Q	Q				1.14/1.73/3.41
	ADA4062-2A	ADA4062-4A		±4	<±18	JFET, LT			1	1.4	3.3	2.5	4	73	74	76	36	0.005	0.220	50	20	H				D	D	D/Q		Q		0.75/1.21
	ADA4062-2B			±4	<±18	JFET, LT			1	1.4	3.3	1.5	4	80	80	76	36	0.005	0.220	50	20	H					D					1.25
	ADA4075-2			±4.5	<±18	BP			1	6.5	12	1	0.3	110	106	114	2.8	1.2	2.250	100 nA	40	H					D	D				0.75
ADA4077-1A[6]	ADA4077-2A	ADA4077-4A[6]		±4.5	<±18	BP, LT			1	4	1	0.05	0.25	132	123	125	7	3	0.500	1 nA	22	H				S/D	D/Q			Q		0.77/1.38/2.90
ADA4077-1B[6]	ADA4077-2B			±4.5	<±18	BP, LT			1	4	1	0.025	0.1	132	123	125	7	3	0.500	1 nA	22	H			S		D					2.36/3.81
ADA4084-1[6]	ADA4084-2	ADA4084-4[6]		±2	<36	LT	•	•	1	9.9	4.6	0.1	0.5	106	110	110	3.9	0.55	0.750	300 nA	30	H			S	D	D	S/D/Q		Q		1.50/2.85/4.45
	ADA4091-2	ADA4091-4		2.7	<36	OVP, LT	•	•	1	1.27	0.46	0.25	3	104	108	110	25		0.250	55 nA	20	H					D	D/Q		Q		2.22/3.60
	ADA4096-2	ADA4096-4		3	<36	OVP, LT	•	•	1	0.8	0.4	0.3	1	82	100	110	27	0.2	0.075	25 nA	10	H				D		D/Q		Q		1.87/2.70
ADA4610-1A[6]	ADA4610-2A	ADA4610-4A[6]		±4.5	<±18	JFET		•	1	9.3	25	1	1	106	106	104	7.3		1.850	25	79	H				S/D	S/D/Q	D		Q		—/1.43/3.65
ADA4610-1B[6]	ADA4610-2B			±4.5	<±18	JFET		•	1	9.3	25	0.4	0.5	106	106	104	7.3		1.850	25	79	H					S/D					—/3.47
ADA4638-1				9	<33	ZD	N	•	1	1.5	1.5	0.0045	0.0125	130	120	140	66	0.1	1.050	90	38	H					S	S				1.28
	ADTL082A	ADTL084A		±4	<±18	JFET	P		1	5	20	5.5	10	80	80	100	16		1.800	100	27	H				D	D/Q			Q		0.43/0.92
OP113E	OP213E	OP413E		4	<36	LT	N		1	3.4	1.2	0.075	0.2	100	103	120	4.7	0.4	3.000	600 nA	40	I					S/D/Q				D	3.49/6.66/9.60
OP113F	OP213F	OP413F		4	<36	LT	N		1	3.4	1.2	0.15	0.2	96	100	120	4.7	0.4	3.000	600 nA	40	I					S/D/Q				D	1.76/2.10/4.52
OP1177	OP2177	OP4177		±2.5	<±18	LT			1	1.3	0.7	0.06	0.2	120[1]	120	120	7.9	0.2	0.500	2 nA	25	H				S/D	S/D/Q			Q		0.81/1.53/3.60
	OP282	OP482		±4.5	<±18	JFET, LT	P		1	4	9	3	10	70	110	86	36	0.01	0.250	100	10	I				D	D/Q				Q	1.42/2.16
OP184	OP284	OP484		3	<36	LT	•	•	1	4.25	4	0.1	0.2	86	90	103	3.9	0.4	2.000	350 nA	10[1]	H					S/D/Q				D/Q	1.66/3.19/5.31
	OP295	OP495		3	<36	LT	N	•	1	0.085	0.03	0.5	1	90	90	120	45	0.1	0.175	20 nA	25[1]	H					D/Q				D/Q	2.22/4.53
OP97E	OP297E			±2	<±20	LT			1	0.9	0.2	0.025	0.2	114	114	109	14	0.02[1]	0.380	100	10	I									S/D	3.84/8.69
OP97F	OP297F	OP497F		±2	<±20	LT			1	0.9	0.2	0.075	0.3	110	110	106	14	0.02[1]	0.380	150	10	I					S/D/Q				S/D/Q	1.28/3.50/6.16
	OP297G	OP497G		±2	<±20	LT			1	0.9	0.2	0.2	0.3	110	110	106	14	0.02[1]	0.625	200	10	I					D/Q				D/Q	2.61/4.49
OP777	OP727	OP747		2.7	<36	LT	N	•	1	0.7	0.2	0.1	0.3	110	120	120	15	0.13	0.290	10 nA	30[1]	I				S	S/D/Q			D/Q		1.20/1.80/3.31
AD797A				±5	<±18	ZZ			1	8	20	0.04	0.2	114	114	120	0.9	2	10.500	1500 nA	80	I					S				S	4.32
AD797B				±5	<±18	ZZ			1	8	20	0.08	0.2	120	120	126	0.9	2	10.500	900 nA	80	I					S				S	5.88
AD8675				±5	<±18	LT		•	1	10	2.5	0.075	0.2	114	120	123	2.8	0.3[1]	2.900	2 nA	35	H				S	S					1.18
AD8677				±4	<±18	LT			1	0.6	0.2	0.13	0.5	120	115	120	10	0.074	1.300	1 nA	30	H			S		S					0.76
ADA4627-1A				±4	<±18	JFET, LT			1	19	84	0.3	1	100	103	106	6.1	0.0025	7.500	5	55	H					S	S				3.47
ADA4627-1B				±4	<±18	JFET, LT			1	19	84	0.2	1	106	106	112	6.1	0.0016	7.500	5	55	H					S	S				4.78
ADA4637-1A				±4	<±18	JFET, LT			5	79	170	0.3	1	100	103	106	6.1	0.0025	7.500	5	55	H					S	S				3.47
ADA4637-1B				±4	<±18	JFET, LT			5	79	170	0.2	1	106	106	112	6.1	0.0016	7.500	5	55	H					S	S				4.78

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[3] N = negative rail input, P = positive rail input.
[4] C = commercial (0°C to +70°C), H = extended industrial (–40°C to +125°C), I = industrial (–40°C to +85°C).
[5] S = single, D = dual, Q = quad.
[6] In development.

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Part Number			Shutdown	Supply Voltage (V)		Technology Feature[2]	Rail-to-Rail[3]		A_{CL} Min	BW @ A_{CL} Min (MHz)	Slew Rate (V/µs)	V_{OS} Max (mV)	TCV_{OS} Typ (µV/°C)	CMRR Min (dB)	PSRR Min (dB)	A_{VO} Min (dB)	Noise @ 1 kHz (nV/√Hz)	Noise @ 1 kHz (pA/√Hz)	I_S/Amp Max (mA)	I_B Max (pA)	I_{SC} (mA)	Temp Range[4]	MicroCSP[5]	SC70[6]	SOT-23[6]	MSOP[5]	SOIC[5]	LFCSP[5]	BBFPP	TSSOP[5]	PDIP[5]	Price @ 1k (OEM U.S.)
Single	Dual	Quad		Min	Max[1]		In	Out																							
	AD8436			3	<±36	High Temp		•	1	9.7	5	0.25	0.35	100	103	100	4.2	0.6	1.3	200 nA	18	S					175°C		210°C		D	99.00/110.00
ADA4700-1				±5	±55				1	3.5	20	2	2	103	110	103	14.7	0.4	2.2	30 nA	75	I					S					2.69
OP07																																
OP07D				±4	<±18	LT			1	0.6	0.2	0.15	0.5	120	115	120	10	0.074	1.300	1 nA	30	H					S				S	0.45
OP27G				±4.5	<±18	ZZ			1	8	2.8	0.1	0.3	100	140	116	3.2	0.4	5.700	80 nA	30	I					S				S	1.19
OP37G				±4.5	<±18	ZZ			5	40	17	0.1	0.3	100	140	116	3.2	0.4	4.700	75 nA	30	I					S				S	1.14
		ADA4092-4		2.7	<36	OVP, LT	•	•	1	1.4	0.4	1.5	2.5	90	98	116	30		0.200	60 nA	20	H								Q		2.50
	OP292G	OP492G[1]		±4.5	<±18	LT			1	4	4	2	4	78	75	88	15	0.7[1]	1.400	700 nA	11	H					D/Q					1.57/2.71
	OP270E	OP470E[2]		±4.5	<±18	LT			1	5	2.4	0.075	0.2	106	110	123	3.2	0.6	2.000	20 nA		I									D/Q	8.25/12.03
	OP270F			±4.5	<±18	LT			1	5	2.4	0.15	0.4	100	105	120	3.2	0.6	2.000	40 nA		I									D	6.19
	OP270G	OP470G[1]		±4.5	<±18	LT			1	5	2.4	0.25	0.7	90	104	117	3.2	0.6	2.000	60 nA	30[1]	I					D/Q				D/Q	2.80/4.43
		OP471G		±4.5	<±18	LT			1	5	8.5	1.8	4	90	95	110	7.6	0.4	2.750	60 nA	20[1]	I					D/Q				D/Q	4.93
		OP467		±4.5	<±18	LT			1	28	170	0.5	3.5	80	96	83	6	0.8	2.500	600 nA	40[1]	I					Q				Q	7.48
OP193F	OP293F			2.4	<36	LT	N[2]		1	0.035	0.015	0.15	0.2	97	97	114	65	0.05	0.030	20 nA[1]	25	H					S/D					1.65/2.49
OP90	OP290[1]	OP490[1]		1.6	<36	LT	N	N	1	0.02	0.012	0.45	5	90	100	112	60	0.7	0.020	25 nA	20	I					S/Q				S/D/Q	1.89/3.05/4.16
AD549J				±2.5	<±18	JFET, ZZ			1	5	3	1	20	80	80	109	35	0.22	0.700	250 fA	20	C	TO-99									18.26
AD549K				±2.5	<±18	JFET, ZZ			1	5	3	0.25	5	90	90	109	35	0.22	0.700	100 fA	20	C	TO-99									23.48
	OP275			±4.5	<±22	BiJET			1	9	22	1	2	80	85	108	6	1.5	2.500	350 nA	14	I					D				D	1.00
AD711J	AD712J	AD713J		±5	<±18	BiJET			1	4	20	2	7	76	76	103	18	0.01	3.400	50	25	C					S/D/Q				S/D/Q	1.20/1.66/4.62
AD711K	AD712K			±5	<±18	BiJET			1	4	20	0.5	5	80	80	106	18	0.01	3.000	50	25	C					S/D				S/D	2.10/3.36
	OP285			±4.5	<±22	BiJET			1	9	22	0.25	1	80	85	108	6	0.9	2.500	350 nA	30	I					D				D	2.45

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 ST07377-.5-9/14(F)

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